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Computer Organization & Architecture SOLUTION

INTR is a signal which if enabled then microprocessor has interrupt enabled it receives high INR signal & activates INTA signal, so another request can't be accepted till CPU is busy in servicing interrupt.

Hence (A) is correct option. Question. 12

Computer Organization and Architecture

1.1 Computer architecture. refers to those attributes of a system visible to a programmer or, put another way, those attributes that have a direct impact on the logical execution of a program. Computer organization. refers to the operational units and their interconnections that realize the architectural specifications.

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CHAPTER 2 COMPUTER EVOLUTION AND PERFORMANCE ANSWERS TO QUESTIONS 2.1 In a stored program computer, programs are represented in a form suitable for storing in memory alongside the data.

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2 CHAPTER solutions David Money Harris and Sarah L. Harris, Digital Design and Computer

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- 13 - 2.9 When the parallel load input = 1, the clock pulses go through the AND gate and the data inputs are loaded into the register when the parallel load input = 0, the output of

Computer System Arcitecture

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