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Communication System

A system on a chip

(SoC / ? ? s ? o ? ? s

i ? / es-oh-SEE or / s

? k / sock) is an

integrated circuit

(also known as a

"chip") that

integrates all or

most components of

a computer or other

electronic

system. These

components almost

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always include a central processing unit (CPU), memory, input/output ports and secondary storage – all on a single substrate or microchip, the size of a coin.

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verification, this
chapter focuses on
two major aspects:
verifying the on-chip
communication
architecture
specification and
implementations,
and verifying
whether IP
(intellectual
property) cores are
compliant with the

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protocol to which
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they will be
connected in an
Systems On
system-on-chip
Silicon
(SoC) design.

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Communication Architectures is a comprehensive reference on concepts, research and trends in on-chip communication architecture design. It will provide readers with a comprehensive survey, not available elsewhere, of all current standards for on-chip

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NoC technology
applies the theory
and methods of
computer
networking to on-
chip communication
and brings notable

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improvements over
conventional bus
and crossbar
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come in many
network topologies,
many of which are
still experimental as
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of silicon area. In

the past various
decoder

architectures have
been proposed to
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throughput for
silicon area and
energy.

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mapping: This refers
to the process of
associating abstract
system-level
communications

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with physical
communication
paths in the CA
topology [8]. 7.2.2

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respect to topology,
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architectures can be
classified as

follows:

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have evolved to
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offers following
tools to help you
find materials and
properties data.

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Material Property
Search . Also known
as Data Search, find
materials and
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information from
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have been designed to

support all inter-
component

communication in a
SoC design. These

communication
architecture fabrics
have a critical

impact on the power
consumption,

performance, cost
and design cycle

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time of modern SoC
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designs.

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Modern system-on-
chip (SOC) designs
consist of numerous
heterogeneous
components
integrated onto a
single chip

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(embedded CPUs, dedicated hardware, FPGAs, embedded memories, etc). The on-chip communication is becoming the bottleneck for these SOC designs most of which employ shared-bus based communication architecture.

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Improvements in process technology have effectively shrunk board-level components so they can be integrated on a single chip. New on-chip communication architectures have

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been designed to support all Over the past decade, system-on-chip (SoC) designs have evolved to address the ever increasing complexity of applications, fueled by the era of digital convergence.

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2. NoC Architecture.

The advanced
Network-on-Chip
developed by Arteris
employs system-
level network

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techniques to solve onchip traffic transport and management challenges. As discussed in the previous section and shown in Figure 1, synchronous bus limitations lead to system segmentation and tiered or layered bus architectures.

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