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Abstract. In this introductory paper to the field, it is our goal to provide a new unified look at synthesis problems that is independent from the level of abstraction like system, RTL, and logic (for refinements targeted to hardware), or process- and basic block level (for refinements targeted to software).

Synthesis and Optimization of Digital Hardware/Software ...

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Logic optimization, a part of logic synthesis in electronics, is the process of finding an equivalent representation of the specified logic circuit under one or more specified constraints. Generally the circuit is constrained to minimum chip area meeting a prespecified delay.

Logic optimization - Wikipedia

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