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Chap. 2 2 VLSI Digital Signal Processing Systems | Textbook: | K.K. Parhi, VLSI Digital Signal Processing Systems: Design and Implementation, John Wiley, 1999

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VLSI Digital Signal Processing Systems. ... bit and digit-level processing implementations will be covered in detail with strong emphasis on low-power design and high speed implementation. Unit Learning Outcomes (ULO) Students who successfully complete this unit will be able to: 1. Design and implement digital filters in Direct Form I & II as ...

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Keshab K. Parhi - Google Scholar Citations

He has published over 650 papers, is inventor or coinventor of 31 issued US Patents, has authored the text book VLSI Digital Signal Processing Systems: Design and Implementation (Wiley, 1999), and is the co-editor (with Takao Nishitani) of the reference book Digital Signal Processing for Multimedia Systems (CRC Press, March 1999).

Keshab K. Parhi

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